

1     **IN THE CLAIMS**

2  
3             The following listing of claims will replace all prior versions, and listings, of claims in the subject  
4 application:

5  
6     --1. - 21. (**Canceled**)

7  
8     22. (**New**)     A system for providing video signal compensation, said system comprising:

9             a video signal compensating circuit for receiving video signal components of a video signal  
10             including red, green and blue video signals from a remote video source, determining a  
11             skew in receipt of said video signal components, and determining one or more delays to  
12             apply one or more of said components; and  
13             a delay circuit coupled to said video signal tuning circuit for applying said delay or said delays  
14             said components.

15  
16     23. (**New**)     A system according to claim 22, further comprising a signal injection circuit for injecting  
17 a plurality of test pulses for receipt by said video signal compensating circuit for said determining said  
18 delay or delays.

19  
20     24. (**New**)     A system according to claim 23, wherein each of said test pulses is a square wave.  
21

1     25. (New)     A system according to claim 23, wherein said signal injection circuit injects said test  
2     pulses upon receipt of a control signal from said video signal compensating circuit.

3  
4     26. (New)     A system according to claim 22, further comprising memory coupled to said video  
5     signal compensating circuit for storing values of said delays.

6  
7     27. (New)     A system according to claim 26, wherein said video signal compensating circuit  
8     measures said skew by comparing a combined amplitude of said test pulses to a reference amplitude.

9  
10    28. (New)     A system according to claim 27, wherein said video signal compensating circuit  
11    determines said delay or said delays by measuring said skew for each combination of said components  
12    received utilizing said delay circuit, storing results of said measuring in said memory, comparing said  
13    results to said reference amplitude, and calculating said delay or said delays closest to said reference  
14    amplitude.

15  
16    29. (New)     A system according to claim 22, wherein said delay circuit includes at least one  
17    inductor-capacitor circuit.

18  
19    30. (New)     A system for according to claim 22, wherein said delay circuit includes at least one  
20    printed circuit board comprising at least one printed delay circuit.

1     31. (New)     A system according to claim 22, wherein said delay circuit includes a red delay circuit,  
2     a green delay circuit and a red delay circuit, each of said red, green and blue delay circuits being  
3     coupled to video signal compensating circuit.

4  
5     32. (New)     A system according to claim 22, further comprising a override circuit for providing  
6     manual adjustment of said delay circuit.

7  
8     33. (New)     A switching system including circuitry for providing compensation of video signals  
9     including red, green and blue components, said system comprising:

10           a computer interface device for transmitting test pulses and video signals, said computer  
11           interface device including a signal injection circuit for generating said test pulses;

12           a user interface device coupled to said computer interface device, said user interface device  
13           including a signal receiving circuit for receiving said test pulses and said video signals from a remote  
14           video source, and a delay circuit for determining a skew in receipt of said video signal components and  
15           for determining one or more delays to apply one or more of said components.

16  
17     34. (New)     A system according to claim 33, wherein said delay circuit includes at least one  
18     inductor-capacitor circuit.

19  
20     35. (New)     A system according to claim 33, wherein said delay circuit includes at least one printed  
21     circuit board comprising at least one printed delay circuit.

1     36. (New)     A system according to claim 33, wherein each of said test pulses is a square wave  
2     pulse.

4     37. (New)     A system according to claim 33, wherein said computer interface device is coupled to  
5     said user interface device via at least one Category 5 cable.

7     38. (New)     A system according to claim 33, further comprising a switch for selecting transmission  
8     of either said test pulses or said video signals.

10    39. (New)     A system according to claim 38, further comprising a control circuit for generating a  
11    control signal to control said switch.

13    40. (New)     A system according to claim 33, further comprising a composite switch for creating  
14    composite signals comprising said test pulses and said video signals.

16    41. (New)     A system according to claim 40, further comprising an extract circuit for extracting said  
17    test pulses from said composite signals.

19    42. (New)     A system according to claim 40, further comprising an extract circuit for extracting said  
20    video signals from said composite signals.

1     **43. (New)**     A method for compensating for skew introduced during transmission of video signals  
2     having red, green and blue components, said method comprising the steps of:  
3             generating test signals at a computer interface, said signals including one such signal for each of  
4             said red, green and blue components;  
5             receiving said test signals at a user interface;  
6             calculating a difference of time in said receiving;  
7             determining a delay for application to one or more of said components;  
8             producing a signal for introducing said delay; and  
9             applying said delays to one or more of said components.